1 <u>CLAIMS</u>

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3 1. A PLD with decryption and structure for preventing design relocation

4 comprising:

5   a decryptor for decrypting an encrypted bitstream;

6   an address indicator for indicating an address into which configuration

7   data will be loaded; and

8   a decryption algorithm implemented by the decryptor, wherein the

9   decryption algorithm uses data from the address indicator for

10   decrypting the encrypted bitstream.

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12 2. The PLD of Claim 1 wherein the address indicator is an initial address

13 indicator.

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15 3. The PLD of Claim 2 wherein the initial address indicator is a frame address for

16 indicating a starting frame of the PLD into which configuration data will be

17 loaded.

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19 4. The PLD of Claim 1 wherein the decryption algorithm comprises the DES

20 algorithm.

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22 5. The PLD of Claim 4 wherein the DES algorithm includes a cipher block

23 chaining algorithm and the address indicator is placed into a starter value of the

24 cipher block chaining algorithm.

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26 6. The PLD of Claim 4 wherein the DES algorithm includes a cipher feedback

27 mode algorithm and the address indicator is placed into a starter value of the

28 cipher block chaining algorithm

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30 7. The PLD of Claim 1 wherein the address indicator specifies a constant value.

1   8. The PLD of Claim 1 wherein the address indicator is loaded with a value in the

2   bitstream.

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4   9. The PLD of Claim 8 wherein the value in the bitstream is encrypted.

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6   10. The PLD of Claim 8 wherein the value in the bitstream is not encrypted.